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Signal Integrity for PCB Designers

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Signal Integrity for PCB Designers

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Signal Integrity for PCB Designers Vikas Shukla
The book Signal Integrity for PCB Designers helps you understand the effects of the layout and interconnect on the integrity of the signal. When the speed of the signal increases, a designer must be aware of the high speed issues of ringing, power supply noise, crosstalk, electromagnetic inteference. A PCB Designer must understand the effect of the components, traces, layer stack up on the real world PCB design. The Signal Integrity topics covered in the book includes differential signaling, crosstalk, transmission line, impedance calculation, layer stack up, design for EMI. After reading the book you should also be able to run simple simulation. The book elevates a PCB designer to a level where they can understand the challenges presented in the design of a high speed PCB.The book will also help circuit board designers and managers who want to update their knowledge about high speed design.

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